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Intended Audience

This release is targeted for OEM/ODM customers developing LakeField and TigerLake platform designs utilizing THC and Intel® Precise Touch and Stylus Technology.

Customer Support

For OEM/ODM technical support regarding pre-release Intel® Precise Touch and Stylus Technology, contact your assigned Intel enabling engineer.

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1 Introduction

This document provides system requirements, installation instructions, issues and limitations, and legal information for the Intel® Precise Touch driver for Microsoft Windows.

To learn more about this product, see:

- New features listed in the [New in this Release](#) section below, or in the help.

- Reference documentation listed in the [Related Documentation](#) section below
- Post-OS Installation is accomplished by utilizing the context menu (right-click) of the IntelTCHBase.inf file and selecting 'Install'.

2 New in This Release

New Features

- No feature changes in this release.

Changes to Existing Features

- No changes to existing features in this release.

Unsupported or Discontinued Features

- No feature changes in this release.

3 Fixed Issues

===== 3.0.0.204 =====

[FIX] Remove ExitDoze flow from the PIO Write Function

===== 3.0.0.203 =====

[FIX] Enable Tx Queue only after TIC is at enabled state

[FIX] Move out InputDoze lock from HostInitiatedDozeExit and manually take it before calling the function, lock WriteDma entry with same lock

===== 3.0.0.191 =====

[FIX] WriteDMA Start bit needs to have a lock. Otherwise ISR/DPC could simultaneously be doing a read-modify-write on Write DMA CNTRL register leading to undefined behavior

[REMOVE] Revert "Spibus lock really needs to protect the dozeexit flow and doze entry" (3.0.0.185)

===== 3.0.0.184 =====

[UPDATE] Sign ITouch Driver using OWR Certificate

===== 3.0.0.180 =====

[FIX] We no longer need to unquiesce based on threshold. Only unquiesce after processing all outstanding buffers (i.e., free buffers are 16)

[FIX] Move HandleHidIoctl to Non-Paged segment due DPC lock usage on IOCTL_HID_READ_REPORT

[FIX] Adding read lock to protect from simultaneous processing of Read Ioctl and Read DMA interrupt

[BUILD] Remove Centaurus build, Add Build for TIC input descriptor only

4 Known Issues

- None

5 Non-Intel Issues

Please contact your touch sensor/panel provider for any known issues with this HID driver release.

6 Related Documentation

Document ID	Title
	intel precise touch and stylus thc-spi eds -rev1.0
	Intel(R)THC Integration Guide_1.0
	LakeField BIOS Writers Guide TigerLake BIOS Writers Guide

7 Where to Find the Release

This release can be found on the Validation Internet Portal (VIP): <https://platformsw.intel.com>

How to Install this Release

Installation is accomplished by unzipping the single “.zip” file and executing the Setup.exe program included, and following the on-screen instructions

8 Release Content

All content, including a copy of this 'release notes' document' are included in a single ".zip" file.

External Dependencies

Please contact your touch sensor/panel provider for any hardware or firmware required.

9 Hardware and Software Compatibility

- 11th Generation Intel® Core™ Processor U Series-based Platform and newer
- 11th Generation Intel® Core™ Processor Y Series-based Platform and newer
- 11th Generation Intel® Core™ Processor H Series-based Platform and newer

Supported Operating Systems

Microsoft Windows 10, Windows 10x

10 Acronyms and Terms

The following acronyms and terms are used in this document (arranged in alphabetic order):

Acronym/Term	Description
BKC	Best Known Configuration
FW	Firmware
HID	Human Interface Device
HECI	Host Embedded Controller Interface
ME	Manageability Engine
OEM / ODM	Original Equipment Manufacturer / Original Design Manufacturer
VIP	Validation Internet Portal
THC	Touch Host Controller

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